

V-Cell

Halfcut Series



TSS65TN

6" Mono c-Si Solar Cell



Physical Characteristics

Dimensions 156.75mm X 156.75 mm ± 0.25mm

Thickness(Si) 180 μm ± 20 μm, 200 μm ± 30 μm

Front(-) Alkaline texturized surface with silicon nitride anti-reflecting coating

Color: Dark Blue, Blue, Sky Blue

5 X 0.7 mm ± 0.1 mm wide bus bars

Distance between bus bars : 31.2 mm

Back(+) Aluminum back surface field
5x4 soldering pads, 1.5 mm±0.15 mm wide bus bars

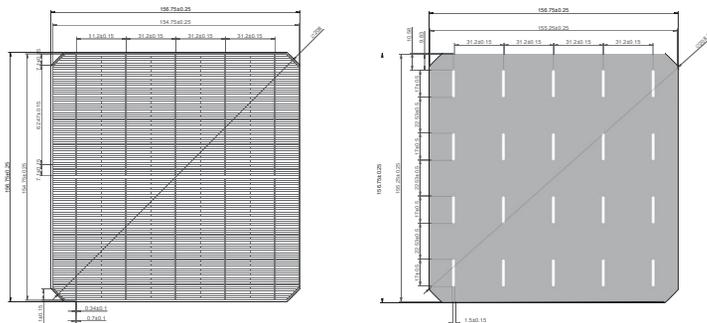
Distance between bus bars : 31.2 mm

Features

- High Cell-To- Module ratio through precise cell conversion efficiency sorting, classified efficiency grade by both minimum power and current.
- Excellent electrical long-term stability and reliability by using of best raw materials and through strict quality inspection control.
- Low breakage rate by using high qualified and stable wafers.
- High quality homogeneous appearance by sorting into defined color classes.
- 100% screened for reverse current and shunt resistance.
- Excellent passivation quality of the rear side compared to the traditional solar cell is clearly visible in the long wavelength regime.
- The best solution for PV module with above 280W outputs.

Quality Control and Professional Service

- Regular calibration of test equipment using Fraunhofer ISE reference cell.
- Environmental friendly due to REACH-SVHC and RoHS compliances.
- Professional on-site service and support for module certification.
- Regular light source AAA class calibration for stable conversion efficiency.
- Lowest LID by periodic monitoring and superior wafer incoming control.



Electrical Characteristics

Efficiency Code	218	217	216	215	214	213	212	211	210	209	202
Efficiency	Eff(%)	21.80	21.70	21.60	21.50	21.40	21.30	21.20	21.10	21.00	20.20
Power	Pmpp(W)	5.33	5.30	5.28	5.25	5.23	5.20	5.18	5.16	5.13	4.94
Max. Power Current	Imp(A)	9.250	9.227	9.211	9.196	9.180	9.165	9.149	9.134	9.121	9.081
Short Circuit Current	Isc(A)	9.758	9.736	9.722	9.708	9.695	9.678	9.660	9.645	9.634	9.599
Max. Power Voltage	Vmpp(V)	0.576	0.575	0.573	0.571	0.570	0.568	0.566	0.564	0.563	0.543
Open Circuit Voltage	Voc(V)	0.675	0.674	0.673	0.671	0.669	0.667	0.665	0.664	0.662	0.642

Standard test condition: AM1.5, 1000W/m², 25°C
Average accuracy of all tests is +/-1.5% rel.

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Temperature Coefficients

Current Temperature Coefficient	$\alpha(I_{SC})$	0.0459%/K
Voltage Temperature Coefficient	$\beta(V_{OC})$	-0.2905%/K
Power Temperature Coefficient	$\gamma(P_{max})$	-0.3682%/K

Standard test condition: AM1.5, 1000W/m², 25°C

Processing Recommendations

Solder Joint

Copper ribbons coated with 15~25µm:
62%Sn/36%Pb/2%Ag or 60%Sn/40%Pb

Standard test condition: AM1.5, 1000W/m², 25°C

Solderability

Peel Strength Minimum

> 1.25 N/mm

Soldering results may differ due to different flux, ribbons, soldering methods, and parameters.

Qualifications and Certificates



RoHS Compliance
SVHC tested

TSEC Corporation

Taipei Headquarters

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New Taipei City 23143, Taiwan, R.O.C

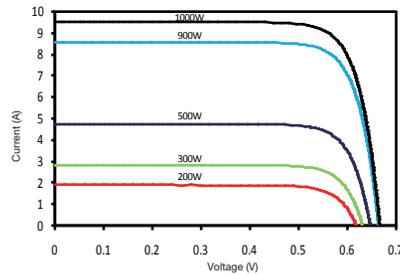
t 886 2 2912 2199 f 886 2 2917 5399 m sales@tsecpv.com

Hsinchu Plant

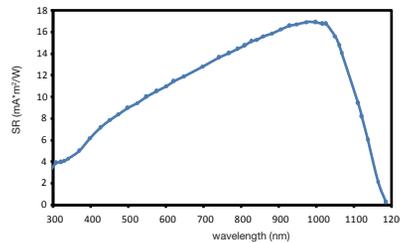
No.85, Guangfu N. Rd., Hukou Township, Hsinchu County 30351,
Taiwan, R.O.C. (Hsinchu Industrial Park)

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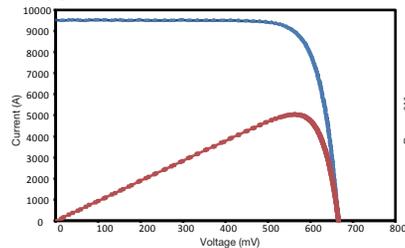
Typical Current-Voltage Curve



Typical Spectral Response



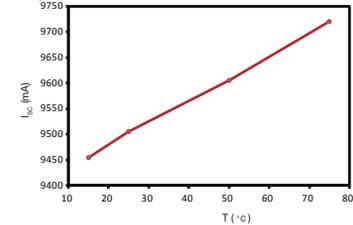
Typical IV-Power Curve



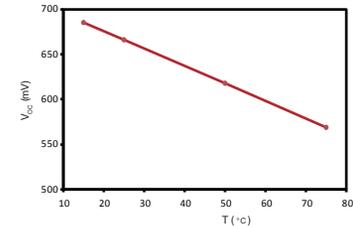
* All data measured under standard testing condition (STC):
1000 W/m², AM 1.5, 25 °C.
* All figures bear ±2% tolerance.
* Reference cell calibrated by the Fraunhofer ISE in Freiburg.

Calculated Temperature Coefficients

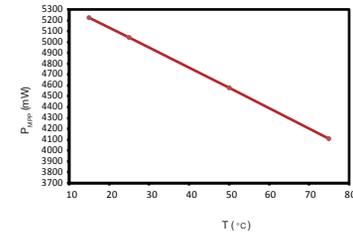
Short Circuit Current $K(I_{sc})=(4.36\pm 0.37) \text{ mA/K}$
 $K(I_{sc})=(-0.0459\pm 0.0039) \% / \text{K}$



Open Circuit Voltage $TK(V_{oc})=(-1.935\pm 0.045) \text{ mV/K}$
 $TK(V_{oc})=(-0.2905\pm 0.0068) \% / \text{K}$



Power $TK(P_{MPP})=(-18.56\pm 0.42) \text{ mW/K}$
 $TK(P_{MPP})=(-0.3682\pm 0.0084) \% / \text{K}$



Specifications are subject to change without prior notice.
TSEC reserves the rights of final interpretation
and revision of datasheet.

Preliminary version-September 2018