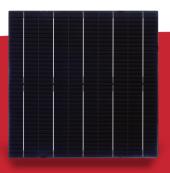
V-Cell

Halfcut Series

TSM65TN

6" Multi c-Si Solar Cell





Dimension 156.75mm X 156.75mm±0.25mm				
Thickness(Si) 180 μm±20 μm, 200μ m ±30 μm				
Acid texturized surface with Silicon nitride anti-reflecting coating				
Color: Dark Blue, Blue, Sky Blue & Light Blue				
5 X 0.7 mm ± 0.1 mm wide bus bars				
Distance between bus bars : 31.2 mm				
Aluminum back surface field				
$\overline{5x4}$ soldering pads, 1.5 mm \pm 0.15 mm wide bus bars				

Distance between bus bars: 31.2 mm



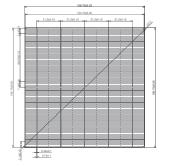


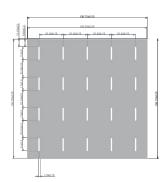
Features

- -High Cell-To- Module ratio through precise cell conversion efficiency sorting, classified efficiency grade by both minimum power and current.
- -Excellent electrical long-term stability and reliability by using of best raw materials and through strict quality inspection control.
- -Low breakage rate by using high qualified and stable wafers.
- -High quality homogeneous appearance by sorting into defined color classes.
- -100% screened for reverse current and shunt resistance.
- -Excellent solderability through high quality conductive materials and regular monitor soldering properties.

Quality Control and Professional Service

- -Regular calibration of test equipment using Fraunhofer ISE reference cell.
- -Environmental friendly due to REACH-SVHC and RoHS compliances.
- -Professional on-site service and support for module certification.
- -Regular light source AAA class calibration for stable conversion efficiency.
- -Lowest LID by periodic monitoring and superior wafer incoming control.





Electrical Characteristics

Efficiency Code		208	207	206	205	204	203	202	200	198	196	192
Efficiency	Eff(%)	20.80	20.70	20.60	20.50	20.40	20.30	20.20	20.00	19.80	19.60	19.20
Power	Pmpp(W)	5.11	5.09	5.06	5.04	5.01	4.99	4.96	4.91	4.87	4.82	4.72
Max. Power Current	Impp(A)	9.072	9.061	9.050	9.039	9.024	9.010	8.996	8.980	8.942	8.941	8.937
Short Circuit Current	Isc(A)	9.557	9.550	9.541	9.531	9.517	9.504	9.491	9.476	9.439	9.436	9.327
Max. Power Voltage	Vmpp(V)	0.563	0.561	0.559	0.557	0.555	0.554	0.552	0.547	0.544	0.539	0.528
Open Circuit Voltage	Voc(V)	0.665	0.664	0.662	0.660	0.659	0.657	0.656	0.651	0.647	0.639	0.632

Standard test condition: AM1.5, 1000W/m², 25°c Average accuracy of all tests is +/-1.5% rel.

TSM65TN 6" Multi c-Si Solar Cell



Current Temperature Coefficient	a(ISC)	0.0511%/K
Voltage Temperature Coefficient	β(VOC)	-0.2906%/K
Power Temperature Coefficient	γ(Pmax)	-0.3654%/K

Standard test condition: AM1.5, 1000W/m2, 25°c

Processing Recommendations

Solder Joint

Copper ribbons coated with 15~25µm: 62%Sn/36%Pb/2%Ag or 60% Sn 40%Pb

Standard test condition: AM1.5, 1000W/m2, 25°c

Solderability

Peel Strength Minimum

>1.25 N/mm

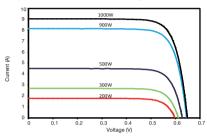
Soldering results may differ due to different flux, ribbons, soldering methods, and parameters.

Qualifications and Certificates

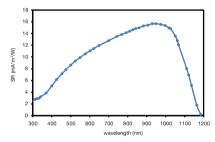


RoHS Compliance SVHC tested

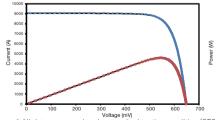
Typical Current-Voltage Curve



Typical Spectral Response

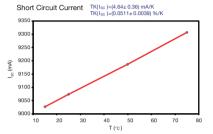


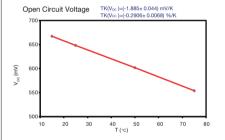
Typical IV-Power Curve

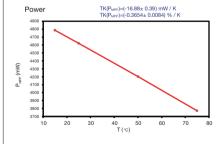


- * All data measured under standard testing condition (STC): 1000 W/m2, AM 1.5, 25 °C.
- * All figures bear ±2% tolerance. * Reference cell calibrated by the Fraunhofer ISE in Freiburg.

Calculated TemperatureCoefficients







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Specifications are subject to change without prior notice. TSEC reserves the rights of final interpretation and revision of datasheet.

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