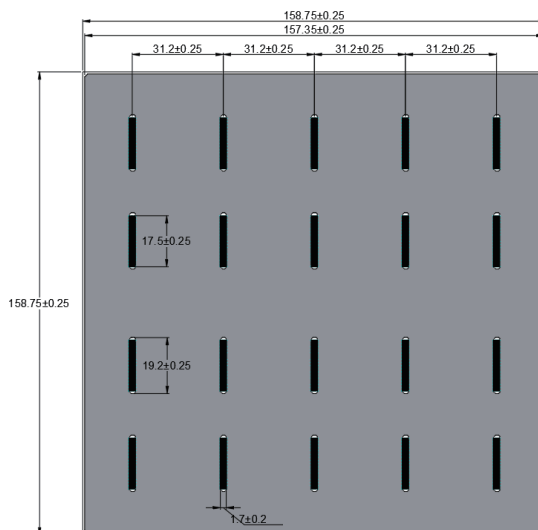


Type Mono-crystalline Silicon Solar Cell

Technical drawing of a rectangular panel with dimensions and tolerances:

- Overall width: 158.75 ± 0.15
- Overall height: 157.35 ± 0.15
- Four vertical sections, each with a width of 31.2 ± 0.15
- Two horizontal sections, each with a height of 0.7 ± 0.1
- Two horizontal sections, each with a height of 0.4 ± 0.1



Dimension	158.75mmx158.75mm±0.25mm
Front Busbar	5BB*0.7mm solid*two segments and half-cell
Finger	122 fingers
Back Busbar	Segmented electrodes 5*4, width*length: 1.7mm*17.5mm

- High conversion efficiency with high reliability
- No light-induced degradation
- Uniform cell performance with stable process control
- Low mismatch of cell performance during encapsulation
- Excellent power generation performance under low irradiation
- Low hot spot effect

- Proper inspection through incoming production, outgoing and -packaging
- 1.0A In the case of reverse 12V, the reverse current is less than 1.0 A
- 100% checked for reverse current and visual appearance
- Reference cell calibrated from Fraunhofer

► Electric Performance

Gear (%)	Efficiency period (%)	Pmpp(W)	Impp(W)	Umpp(W)	Isc (A)	Uoc (V)	FF(%)
21.0	21.0-21.1	5.29	9.497	0.557	9.906	0.657	81.26
21.1	21.1-21.2	5.32	9.534	0.558	9.915	0.660	81.28
21.2	21.2-21.3	5.34	9.536	0.560	9.916	0.662	81.33
21.3	21.3-21.4	5.37	9.555	0.562	9.945	0.662	81.56
21.4	21.4-21.5	5.39	9.557	0.564	9.957	0.662	81.75
21.5	21.5-21.6	5.42	9.593	0.565	9.970	0.664	81.85
21.6	21.6-21.7	5.44	9.594	0.567	9.979	0.665	81.96
21.7	21.7-21.8	5.47	9.613	0.569	10.007	0.666	82.06
21.8	21.8-21.9	5.49	9.618	0.571	10.031	0.666	82.15
21.9	21.9-22.0	5.52	9.634	0.573	10.058	0.667	82.27
22.0	22.0-22.1	5.54	9.652	0.574	10.082	0.667	82.36
22.1	22.1-22.2	5.57	9.665	0.576	10.098	0.668	82.50
22.2	22.2-22.3	5.59	9.681	0.577	10.120	0.668	82.63
22.3	22.3-22.4	5.62	9.726	0.578	10.141	0.669	82.85

Under standard test condition: 1000W/ m2, AM 1.5g, 25°C Specifications and data are for reference only and are subject to change

► Key technology

Cells with efficiency above 21.4% are required to be EL tested; Using thermal oxidation process to anti-PID, Using photoinjection process to anti-LID, Cells for main efficiency are sorted by High and Low voltage(5 efficiency levels will be sorted)

► IV curve

